

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	METHOD FOR FABRICATING INTEGRATED CIRCUITS HAVING BOTH HIGH VOLTAGE AND LOW VOLTAGE DEVICES
Application Number :	
Date :	
First Named Applicant:	Jung-Ching CHEN
Confirmation Number:	
Attorney Docket Number:	NAUP0575USA

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Winston Hsu Registered Number: 41,526	Winston Hsu

Documents being submitted:	Files
us-power-of-attorney-grant	NAUP0575_0_A1-uspoat.xml us-power-of-attorney-grant.dtd us-power-of-attorney-grant.xsl
us-assignment	NAUP0575_0_A1-usassn.xml us-assignment.xsl us-assignment.dtd NAUP0575_0_A1ASS1.tif NAUP0575_0_A1ASS2.tif
us-request	NAUP0575_0_A1-usreq.xml us-request.dtd us-request.xsl
us-declaration	NAUP0575_0_A1-usdecl.xml us-declaration.dtd us-declaration.xsl
us-fee-sheet	NAUP0575_0_A1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	NAUP0575-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd Naup0575usa-01.tif Naup0575usa-02.tif Naup0575usa-03.tif Naup0575usa-04.tif

us-declaration	Naup0575usa-05.tif
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**Comments**